



- S11: (223225) "257"/("659" "704" "710").ccls.
 - S12: (48) S9 and S11
 - S13: (159351) (region\$2 part\$3 area locat\$3 cent\$3 edge perip
 - S14: (159352) (region\$2 part\$3 area locat\$3 cent\$3 edge perip
 - S15: (51680) (cent\$3 edge) with shield\$3
 - S16: (281) S1 and S15
 - S17: (198) S16 and S2
 - S18: (4849) (shield\$3 near3 layer) with (shap\$3 pattern\$3)
 - S19: (5291) (shield\$3 near3 layer) with (shap\$3 pattern\$3)
 - S20: (20) S17 and S19
 - S21: (0) (hallow near3 shap\$3) with (magnetic near3 shield\$3
 - S22: (2) "5939772".pn.
 - S23: (15) "5939772"
 - S24: (0) "10506752"
 - S25: (0) (10/506752).CCLS.
 - S26: (0) "10/506752"
 - S27: (1) "10/506752"
 - S28: (1) "09668922"
 - S29: (2) "6,724,027"
 - S30: (2) "6,515,352"
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DBs

Default operator

☐ Burs
☒ Highlight all hit terms initially

☒ BRS form ☒ ISAR form ☒ Image ☒ Text ☒ HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P				
1	<input type="checkbox"/>	US 5561265 A	19961001	8	Integrated circuit packaging	174/35GC	257/659; 257/E23.114;		Livshits; Boris I. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	US 5939772 A	19990817	7	Shielded package for magnetic devices	257/659	257/660; 257/704;		Hurst; Allan T. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
3	<input type="checkbox"/>	US 6090480 A	20000718	35	Magnetoresistive device	428/332	257/E43.004; 324/252;		Hayashi; Kazuhiko	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
4	<input type="checkbox"/>	US 6452253 B1	20020917	8	Method and apparatus for magnetic shielding of an	257/659	257/687; 257/E23.126;		Tuttle; Mark	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
5	<input type="checkbox"/>	US 6515352 B1	20030204	7	Shielding arrangement to protect a circuit from stray	257/659	257/660; 257/E23.114		Spielberger; Richard K. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
6	<input type="checkbox"/>	US 6625040 B1	20030923	6	Shielded PC board for magnetically sensitive	361/816	174/255; 174/35A;		Tuttle; Mark	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
7	<input type="checkbox"/>	US 6661071 B2	20031209	7	Memory having plural magnetic layers and a	257/422	257/414; 257/421		Lenssen; Kars-Michiel Hubert et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
8	<input type="checkbox"/>	US 6717241 B1	20040406	9	Magnetic shielding for integrated circuits	257/659	257/778; 257/E23.114		Tuttle; Mark	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
9	<input type="checkbox"/>	US 6724027 B2	20040420	11	Magnetic shielding for MRAM devices	257/295	257/422; 257/659;		Bhattacharyya; Manoj K. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
10	<input type="checkbox"/>	US 6744087 B2	20040601	41	Non-volatile memory using ferroelectric gate	257/295	257/328; 257/E21.208;		Misewich; James A. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
11	<input type="checkbox"/>	US 6867469 B2	20050315	24	Magnetic shielding for reducing magnetic	257/421	257/422; 257/425;		Sharma; Manish et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
12	<input type="checkbox"/>	US 6872993 B1	20050329	15	Thin film memory device having local and external	257/259	257/659; 365/158;		Zhu; Theodore et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>

☒ Hits ☐ Details ☒ HTML